PicoZed FMC Carrier

Avnet Engineering Services

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Operational Note: PL PMOD data rates: Open Brain: 2 Mbps
BOARDS POWER

VIN TO 5V POWER SUPPLY

5 CHANNEL SEQUENCED POWER SUPPLY

POWER SEQUENCE:
Vccio_En -> 1V -> 1.2V & 1.8V -> VADJ & 3.3V

VOLTAGE MONITOR HEADER

USE CON2 TO MEASURE VADJ AND GROUND

VAJD SELECTION
(1.8, 2.5V, 3.3V)

WARNING:
CON2 Default: 1.8V (no jumper)

VAJD VOLTAGE SELECTION:

SideScreen:
NOT TO: See user guide.

SOM POWER CONTROL

5/5/2015
Revision Notes:

Revision A:
1) Initial design

Revision B:
1) Updated BOM
2) Updated power circuit for 12V input

Revision C:
1) Production 1st run
2) Modified BOM, CC pins
3) Translator on USB
4) Translators on PL PMODs

Revision C03:
1) Capacitors C147, C148, C151, C152, C153, C154, C162, C163, C181, C182, C183, C184 have been replaced with 0201 Zero Ohm resistors. The schematic shows these parts as resistors symbolically, but remains the capacitor designator to aid the user in finding where the parts have been changed.
2) J5 & J6 new PN: 475890001
3) L16 changed to PN: SRU6025-1R2Y
4) P2 changed to 1888247-1 (SFP+)
5) U10 changed to Abracon ASD3-25.000MHZ-EC-T
6) Two additional rubber bumpers added to the board for stability.
7) Marvel PHY rework to change address from 0x00 to 0x01.
9) Updated JP6 operational note.

Revision Notes:

Mechanicals:

PCB Mounting Holes

Bumper Standoffs