UltraZed-EV SOM
Revision 1
120-Pin Connector (JX3, 4x30 Pins)

DDR4
(4GB, x64)

Dual QSPI
(64MB)

eMMC
(8GB, x8)

PS Reference
Clock

I2C EEPROM
(2Kb)

USB 2.0
ULPI PHY

Ethernet
RGMII PHY

PS-CLK

MIO[0:12]

MIO[13:22]

I2C – MIO[24:25]

MIO[26:51]

MIO[52:83]

MIO[64:77]

GTR[0:3]

GTR_REFCLK[0:3]

PS-Side

Zynq UltraScale+
ZU7EV-FBVB900

PL-Side

DDR4
(1GB, x16)

System Clock
(300MHz OSC)

PL SYSCLK

HD I/O (48)

GTH[0:15]

GTH_REFCLK[0:7]

HP I/O (104)

JTAG (4)

SYSMON (4)

PMBus (3)

PMBus Voltage Regulators
Input: 5 – 12V
Outputs: 0.6V, 0.85V, 1.0V, 1.2V,
1.25V, 1.8V, 2.5V, 3.3V

200-Pin Connector
(JX2, 4x50 Pins)

200-Pin Connector
(JX1, 4x50 Pins)

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DESIGN NOTE: Use ONLY +16V or 33uF capacitor on +VCC for 12V support.
AVNET
Avnet Engineering Services

Project Name:
Size:
PCB Rev:

Sheet:

I2C/PMBUS Address Offset = +7
I2C/PMBUS Address = 0x17/0x47

SCHEMATIC NOTE:
1.8V @ 0.5A

SCHEMATIC NOTE:
0.85V @ 3.0A

[U5]

I2C Address Offset = +7
I2C Address Offset = +7
I2C Address Offset = +8
I2C/PMBUS Address = 0x18/0x48
Assembly:

Mechanicals:

ESD Bag

XXX-XXX-PCB-X
(PCB PN (In Copper))

Mechanicals:

ESD1

Assembly:

Label_ESD1

Label, ESD

NOTE: 31mm Heatsink 
Frame Clip for U1
Utilize MaxiGRIP Tool PN 
#MGT310 for Installation