UltraZed-EG SOM
Revision 1
Termination Resistor should be placed past the last device as DDR4 Control Signals.
Termination Resistors should be placed past the last device as close to the device as possible.

LAYOUT NOTE: Use P-By Routing and Termination for DDR4 Control Signals.

Termination Resistors should be placed past the last device as close to the device as possible.
Mechanicals:
ESD Bag

Assembly:
Label_ESD1
Label, ESD

Mounting Holes:

GND Test Points:

Jumper Shunts:
SH1 2SN-BK-G
SH2 2SN-BK-G
SH3 2SN-BK-G