<table>
<thead>
<tr>
<th>Revision</th>
<th>Description</th>
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<tbody>
<tr>
<td>V A</td>
<td>Mated with PCB V A</td>
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<tr>
<td>V B</td>
<td>Correct PMOD Part, Correct Ref/Sch Lib, Correct Ref/ inp Voltage, Change Mounting Hole Size, Mated with PCB V B</td>
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<tr>
<td>V C</td>
<td>Change Part on REG1 &amp; SW5, set R31, R93, C39 to DNP, Correct net name on PMOD I/O, set J5, J6, J7, J8, J9 to DNP, Change PROG_B button function to SW1-9, Revise circuitry on CARRIER, REG1, REG2 &amp; VCCIO, Revise Power Up Sequence, Add MAX18025 Circuitry, VC00 &amp; DC-DC feedback divider adopt 0.1% tolerance resistor, Add pull low in PROG_EN, Add jumper fit for PG_CARRIER pull up, Add pull point for power (TP6-TP9), Revise value on R21, R91, Revise Pinout on XADC &amp; Bumper, Add jumper Shunt for ROM, Update circuit connections, Mated with PCB Rev C</td>
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<tr>
<td>V D</td>
<td>New circuitry added to replace MAX18025, Added Q11, Q12, BAT54LT1G, and support components (Page 6), Updated TP1-6 to be DNP graphically, Added power switch/jack control, Mated with PCB Rev D, Updated TP1-6 to be DNP graphically</td>
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