Embedded Vision Carrier Card
Rev B – Errata
Rev 1.0, 3/4/2015

Introduction
Thank you for your interest in the Avnet Embedded Vision Carrier Card. Although Avnet has made every effort to ensure the highest possible quality, these kits and associated software are subject to the limitations described in this errata notification.

Be aware that any of the optional workarounds requiring physical modifications to the board are done at the User’s own risk, and Avnet is not liable for poorly performed rework.

Identifying Affected Modules
The products affected by these errata can be identified by the Revision of the Embedded Vision Carrier Card. The revision of the Embedded Vision Carrier Card can be found on the bottom side of the PCB board. The affected PCB board is Revision B, identified as “MBCC-EMBV-PCB-B”.

Errata
PHY Address Set to Broadcast Affects Shared MDIO Bus
Applications Affected
Applications that are affected require a shared MDIO bus. In this case, the design in the Zynq requires a PL GMII-to-RGMII shim core that is required to exist on the same MDIO bus as the on-board PHY.

Description
The PHY Address is determined during configuration via strapping of the configuration pin, CONFIG. This currently sets the PHY address to the on-board PHY to ‘0’. The GMII-to-RGMII shim also requires a PHY address. With the on-board PHY set to ‘0’ it may respond to an MDIO transaction when it could be intended for another device.

Workaround
Boards that have not already been corrected at the Contract Manufacturer can be reworked to add a single jumper wire to change the CONFIG pin to set the proper address for the Embedded Vision Carrier Card’s on-board Marvell PHY.

Step 1 – Remove 0-ohm Resistor R121

Step 2 – Solder Red Jumper Wire from R121 to R119 (NOTE: R119 should not be removed)
New Erratum
Any new erratum found will be posted to the MicroZed website: www.microzed.org

Additional Support
For additional support, please review the discussions and post your questions to the MicroZed Forums at http://microzed.org/forums/microzed-hardware-design

Revision History

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